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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/146,287 05/15/2002 PAT 6,656,764 *MP*

** FOREIGN APPLICATIONS *****

None *MP*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 01/29/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 10	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 2
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Verified and Acknowledged
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TITLE
 Process for integration of a high dielectric constant gate insulator layer in a CMOS device

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